

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	22618380	@ad<"20020628"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:35
L2	5	(Uwe near Daemmrch).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L3	18	(Dieter near Buchholz).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L4	2	L2 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L5	53213	"714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L6	29350	"711"/.clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L7	44716	(define\$2 or stable or safe) near state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L8	69884	(define\$2 or stable or safe) near2 state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L9	2109	((define\$2 or stable or safe) near2 state) same (restor\$4 or recover\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36

## EAST Search History

L10	4864	interrupt adj service adj routine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L11	584	error adj handling adj routine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L12	936	(irregular or undefine\$4) adj state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L13	9783	unuse\$4 near2 (area or location or space or segment\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L14	4864	interrupt adj service adj routine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L15	584	error adj handling adj routine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L16	5424	L14 or L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L17	2109	((define\$2 or stable or safe) near2 state) same (restor\$4 or recover\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L18	5424	L14 or L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L19	60	L17 and L18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36

## EAST Search History

L20	9783	unuse\$4 near2 (area or location or space or segment\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L21	60	L17 and L18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L22	2	L20 and L21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L23	936	(irregular or undefine\$4) adj state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L24	1	L23 and L21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L25	53213	"714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L26	29350	"711"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L27	80335	L25 or L26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L28	69884	(define\$2 or stable or safe) near2 state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L29	80335	L25 or L26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36

## EAST Search History

L30	2504	L28 and L29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L31	2504	L28 and L29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:36
L32	22	L23 and L31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:37
L33	16	1 and 32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/07 12:37


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide



[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Terms used

[restore](#) [data](#) [recovery](#) [interrupt](#) [routine](#) [error](#) [unused](#) [state](#)

Found 61,615 of 177,263

Sort results by


[Save results to a Binder](#)
[Try an Advanced Search](#)

Display results


[Search Tips](#)
[Try this search in The ACM Guide](#)
☐ Open results in a new window

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐

### 1 [Architecture of the IBM system/370](#)



Richard P. Case, Andris Padegs

January 1978 **Communications of the ACM**, Volume 21 Issue 1

Publisher: ACM Press

Full text available: [pdf\(2.78 MB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

**Keywords:** architecture, computer systems, error handling, instruction sets, virtual storage

### 2 [Status report of the graphic standards planning committee of ACM/SIGGRAPH:](#)


[State-of-the-art of graphic software packages](#)

Computer Graphics staff

September 1977 **ACM SIGGRAPH Computer Graphics**, Volume 11 Issue 3

Publisher: ACM Press

Full text available: [pdf\(9.03 MB\)](#)
 Additional Information: [full citation](#), [references](#)

### 3 [Fault Tolerant Operating Systems](#)



Peter J. Denning

December 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 4

Publisher: ACM Press

Full text available: [pdf\(2.69 MB\)](#)
 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

### 4 [Experience Using Multiprocessor Systems—A Status Report](#)

Anita K. Jones, Peter Schwarz



June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

**Publisher:** ACM Press

Full text available: pdf(4.48 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

5 **ARIES: a transaction recovery method supporting fine-granularity locking and partial rollbacks using write-ahead logging**



C. Mohan, Don Haderle, Bruce Lindsay, Hamid Pirahesh, Peter Schwarz

March 1992 **ACM Transactions on Database Systems (TODS)**, Volume 17 Issue 1

**Publisher:** ACM Press

Full text available: pdf(5.23 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

DB2TM, IMS, and TandemTM systems. ARIES is applicable not only to database management systems but also to persistent object-oriented languages, recoverable file systems and transaction-based operating systems. ARIES has been implemented, to varying degrees, in IBM's OS/2TM Extended Edition Database Manager, DB2, Workstation Data Save Facility/VM, Starburst and QuickSilver, and in the University of Wisconsin's EXODUS and Gamma d ...

**Keywords:** buffer management, latching, locking, space management, write-ahead logging

6 **OS and compiler considerations in the design of the IA-64 architecture**



Rumi Zahir, Jonathan Ross, Dale Morris, Drew Hess

November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5

**Publisher:** ACM Press

Full text available: pdf(96.50 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Increasing demands for processor performance have outstripped the pace of process and frequency improvements, pushing designers to find ways of increasing the amount of work that can be processed in parallel. Traditional RISC architectures use hardware approaches to obtain more instruction-level parallelism, with the compiler and the operating system (OS) having only indirect visibility into the mechanisms used. The IA-64 architecture [14] was specifically designed to enable systems which create ...

7 **OS and compiler considerations in the design of the IA-64 architecture**



Rumi Zahir, Jonathan Ross, Dale Morris, Drew Hess

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

**Publisher:** ACM Press

Full text available: pdf(1.15 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Increasing demands for processor performance have outstripped the pace of process and frequency improvements, pushing designers to find ways of increasing the amount of work that can be processed in parallel. Traditional RISC architectures use hardware approaches to obtain more instruction-level parallelism, with the compiler and the operating system (OS) having only indirect visibility into the mechanisms used. The IA-64 architecture [14] was specifically designed to enable systems which create ...

8 **Reliability Issues in Computing System Design**

B. Randell, P. Lee, P. C. Treleaven



June 1978 **ACM Computing Surveys (CSUR)**, Volume 10 Issue 2

**Publisher:** ACM Press

Full text available: [pdf\(3.95 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

9 [Making operating systems more robust: Improving the reliability of commodity](#)



[operating systems](#)

Michael M. Swift, Brian N. Bershad, Henry M. Levy

October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems principles**

**Publisher:** ACM Press

Full text available: [pdf\(262.78 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Despite decades of research in extensible operating system technology, extensions such as device drivers remain a significant cause of system failures. In Windows XP, for example, drivers account for 85% of recently reported failures. This paper describes Nooks, a *reliability subsystem* that seeks to greatly enhance OS reliability by isolating the OS from driver failures. The Nooks approach is practical: rather than guaranteeing complete fault tolerance through a new (and incompatible) OS ...

**Keywords:** I/O, device drivers, protection, recovery, virtual memory

10 [A client-based transaction system to maintain data integrity](#)



William H. Paxton

December 1979 **Proceedings of the seventh ACM symposium on Operating systems principles**

**Publisher:** ACM Press

Full text available: [pdf\(564.24 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes a technique for maintaining data integrity that can be implemented using capabilities typically found in existing file systems. Integrity is a property of a total collection of data. It cannot be maintained simply by using reliable primitives for reading and writing single units—the relations between the units are important also. The technique suggested in this paper ensures that data integrity will not be lost as a result of simultaneous access or as a result of ...

11 [Improving the reliability of commodity operating systems](#)



Michael M. Swift, Brian N. Bershad, Henry M. Levy

February 2005 **ACM Transactions on Computer Systems (TOCS)**, Volume 23 Issue 1

**Publisher:** ACM Press

Full text available: [pdf\(459.98 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Despite decades of research in extensible operating system technology, extensions such as device drivers remain a significant cause of system failures. In Windows XP, for example, drivers account for 85% of recently reported failures. This article describes Nooks, a *reliability subsystem* that seeks to greatly enhance operating system (OS) reliability by isolating the OS from driver failures. The Nooks approach is practical: rather than guaranteeing complete fault tolerance through ...

**Keywords:** I/O, Recovery, device drivers, protection, virtual memory

12 [Status report of the graphic standards planning committee](#)



Computer Graphics staff

August 1979 **ACM SIGGRAPH Computer Graphics**, Volume 13 Issue 3**Publisher:** ACM PressFull text available: [pdf\(15.01 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#)**13** Telecommunications using a front-end minicomputer

Y. Feinroth, E. Franceschini, M. Goldstein

March 1973 **Communications of the ACM**, Volume 16 Issue 3**Publisher:** ACM PressFull text available: [pdf\(770.84 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The use of a front-end minicomputer to provide varied remote terminal access to a large scale computer is considered. The problems of embedding telecommunications I/O within an operating system are discussed, and it is shown how the decentralization of intelligence acquired by front-end processing vastly simplifies the problem. A specific implementation is discussed with emphasis on the main processor-minicomputer link, the hardware-software implementation, the effect on the main processor ...

**Keywords:** front-end processor, minicomputer, remote job entry, remote terminals, telecommunications

**14** Implementation of Argus

B. Liskov, D. Curtis, P. Johnson, R. Scheifer

November 1987 **ACM SIGOPS Operating Systems Review , Proceedings of the eleventh ACM Symposium on Operating systems principles SOSP '87**, Volume 21 Issue 5**Publisher:** ACM PressFull text available: [pdf\(1.34 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Argus is a programming language and system developed to support the construction and execution of distributed programs. This paper describes the implementation of Argus, with particular emphasis on the way we implement atomic actions, because this is where Argus differs most from other implemented systems. The paper also discusses the performance of Argus. The cost of actions is quite reasonable, indicating that action systems like Argus are practical.

**15** Risks to the public in computers and related systems

Peter G. Neumann

April 1990 **ACM SIGSOFT Software Engineering Notes**, Volume 15 Issue 2**Publisher:** ACM PressFull text available: [pdf\(2.07 MB\)](#) Additional Information: [full citation](#), [index terms](#)**16** A network combining packet switching and time division circuit switching in a common system

Joe de Smet, Ray W. Sanders

January 1976 **ACM SIGCOMM Computer Communication Review**, Volume 6 Issue 1**Publisher:** ACM PressFull text available: [pdf\(1.25 MB\)](#) Additional Information: [full citation](#)



17 The Clipper processor: instruction set architecture and implementation



W. Hollingsworth, H. Sachs, A. J. Smith

February 1989 **Communications of the ACM**, Volume 32 Issue 2

**Publisher:** ACM Press

Full text available: pdf(4.67 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Intergraph's CLIPPER microprocessor is a high performance, three chip module that implements a new instruction set architecture designed for convenient programmability, broad functionality, and easy future expansion.

18 Mondrix: memory isolation for linux using mondriaan memory protection



Emmett Witchel, Junghwan Rhee, Krste Asanović

October 2005 **ACM SIGOPS Operating Systems Review , Proceedings of the twentieth ACM symposium on Operating systems principles SOSP '05**, Volume 39 Issue 5

**Publisher:** ACM Press

Full text available: pdf(332.09 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents the design and an evaluation of Mondrix, a version of the Linux kernel with Mondriaan Memory Protection (MMP). MMP is a combination of hardware and software that provides efficient fine-grained memory protection between multiple protection domains sharing a linear address space. Mondrix uses MMP to enforce isolation between kernel modules which helps detect bugs, limits their damage, and improves kernel robustness and maintainability. During development, MMP exposed two kerne ...

**Keywords:** fine-grained memory protection

19 An architecture framework for application-specific and scalable architectures



J. M. Mulder, R. J. Portier, A. Srivastava, R. in't Velt

April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89**, Volume 17 Issue 3

**Publisher:** ACM Press

Full text available: pdf(796.55 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Two major limitations concerning the design of cost-effective application-specific architectures are the recurrent costs of system-software development and hardware implementation, in particular VLSI implementation, for each architecture. The SCalable ARChitecture Experiment (SCARCE) aims to provide a framework for application-specific processor design. The framework allows scaling of functionality, implementation complexity, and performance. The SCARCE framework consists and wil ...

20 File and storage systems: The Google file system



Sanjay Ghemawat, Howard Gobioff, Shun-Tak Leung

October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems principles**

**Publisher:** ACM Press

Full text available: pdf(275.54 KB)

Additional Information: [full citation](#), [references](#), [index terms](#)

**Keywords:** clustered storage, data storage, fault tolerance, scalability

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)



Welcome United States Patent and Trademark Office

[Search Session History](#)[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Wed, 7 Jun 2006, 1:00:07 PM EST

Edit an existing query or compose a new query in the Search Query Display.

Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

- [#1](#) ((data recovery)<in>metadata)
- [#2](#) (data and (restor\* or recover\*)<IN>metadata)
- [#3](#) ((defin\* or stable or safe) and state<IN>metadata)
- [#4](#) (interrupt and (subroutine or routine)<IN>metadata)
- [#5](#) (unuse\* and (location or space or segment or area)<IN>metadata)
- [#6](#) (((data recovery)<in>metadata)) <AND> (((defin\* or stable or safe) and state<IN>metadata))
- [#7](#) (((data recovery)<in>metadata)) <AND> (((defin\* or stable or safe) and state<IN>metadata))) <AND> ((interrupt and (subroutine or routine) <IN>metadata))
- [#8](#) (((data recovery)<in>metadata)) <AND> (((defin\* or stable or safe) and state<IN>metadata)) <AND> ((unuse\* and (location or space or segment or area)<IN>metadata))

Indexed by  
 Inspec®

[Help](#) [Contact Us](#) [Privac](#)

© Copyright 2006 IE